CSC-103-EB-D

Line Differential Protection IED

Product Guide

BEIJING SIFANG AUTOMATION CO., LTD.

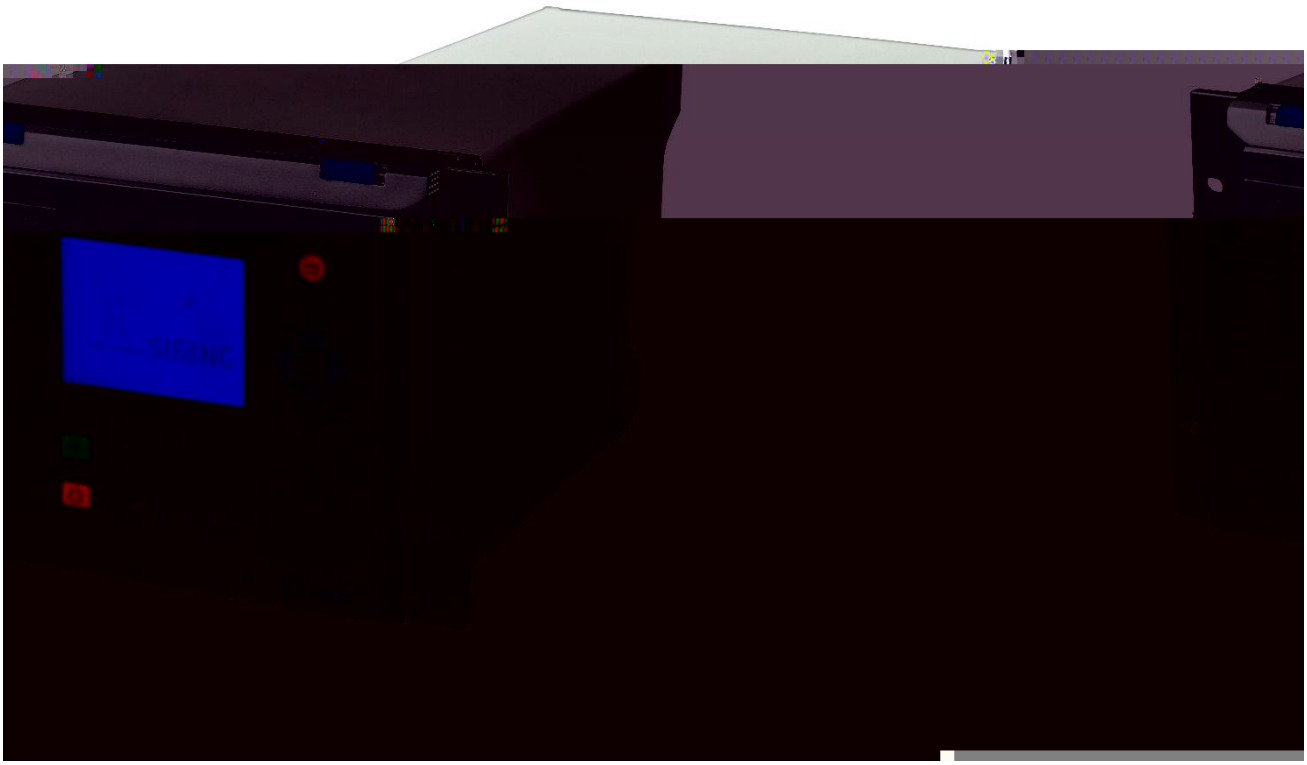
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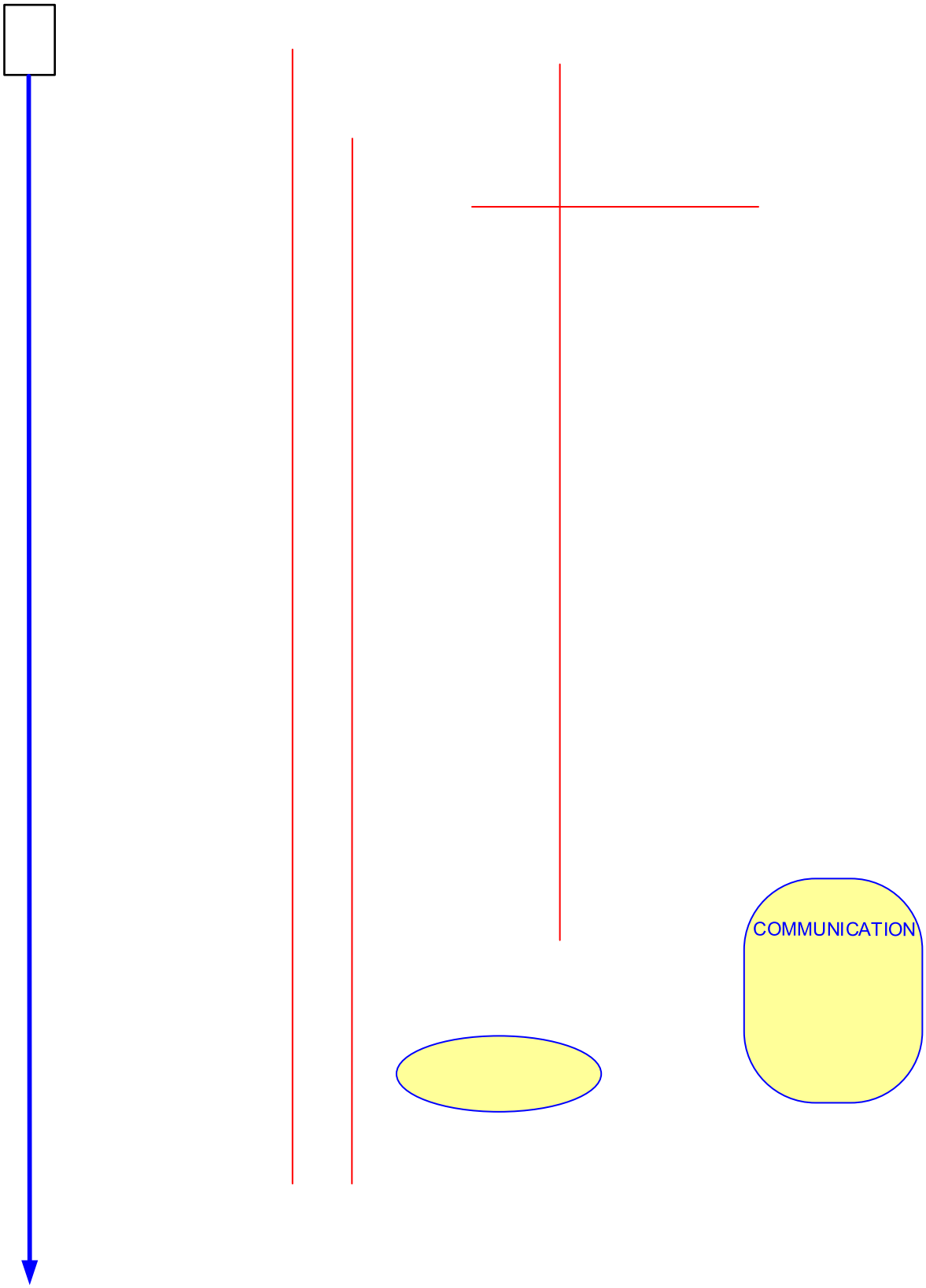
The data contained in this manual is intended solely for the product description and is not to be deemed to be a statement of guaranteed properties. In the interests of our customers, we constantly seek to ensure that our products are developed to the latest technological standards as a result; it is possible that there may be some differences between the hardware/software product and this information product.

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Up to 3 terminals.

Application



Function

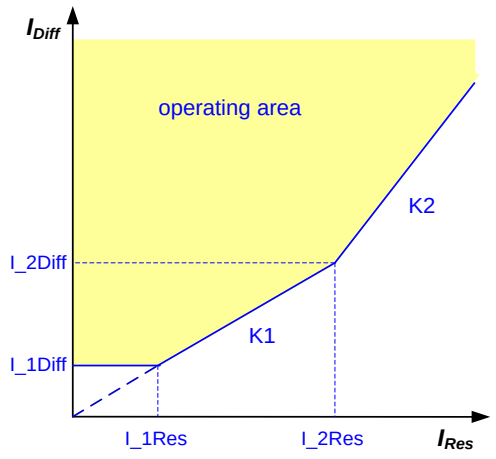
Function

Trip circuit supervision			On TCS module
Programmable logic			
			V, I, F,

Note 1: the number in “Qty.” column is the maximum quantity of function module that can be offered.

87L Line differential protection

Phase segregated differential protection



Automatic CT ratio compensation;

Long-term Differential current supervision

Differential protection CT failure detection

21 Distance protection

Full scheme protection with all phase to phase faults and phase to e7>]TJ/ /P tEe faultloop

arater(t)-4(i)5(c)]TJETQEMC /P <</MCID422>

indeendentl fr/ ( )28(e)11(a)-11cEe ons

Function

25 Synchro-check and energizing check

Synchro-check modes:

- Synchronization check
- Override

Energizing check modes:

- Dead line and dead bus
- Dead line and live bus
- Live line and dead bus

79 Auto-reclosing

4 shots automatic reclosing (selectable)

Internal/ external AR initiation

Single/ three phase AR operation

CB position ready supervision

CB Aux. contact supervision

IEC 62439-3 PRP

MODBUS

Seamless communication by PRP.

IEC 61850-8-1

DNP 3.0;

Tools

AC current measurement

Item	Data

Specification

Specification

Mechanical test

Item	Data

Specification

CE Certification

Item	Data

87L, Line Differential Protection

Item	Data

21, Distance protection

Specification

Max. current setting without inrush current blocking	0.05 I_r to 40.00 I_r
Ratio of second harmonics to	

	2 <I/$I_{SETTING}$ < 20
IEEE/ANSI C37.112	% setting +40ms, at 2 <I/$I_{SETTING}$ < 20,
IEEE/ANSI C37.112	at 2 <I/$I_{SETTING}$ < 20

Specification

	at 80% voltage setting

60255-127

Specification

Reset ratio	

81U, Under frequency protection

Item	Data
Frequency	45.00 to 50.00Hz, step 0.01Hz at F_r = 50Hz 54.00 to 60.00Hz, step 0.01Hz at F_r = 60Hz
Time delay	

Reset = f_{set} - f_{reset}

Disable synchronization check time	

synchronization check time	

79, Auto-reclosing

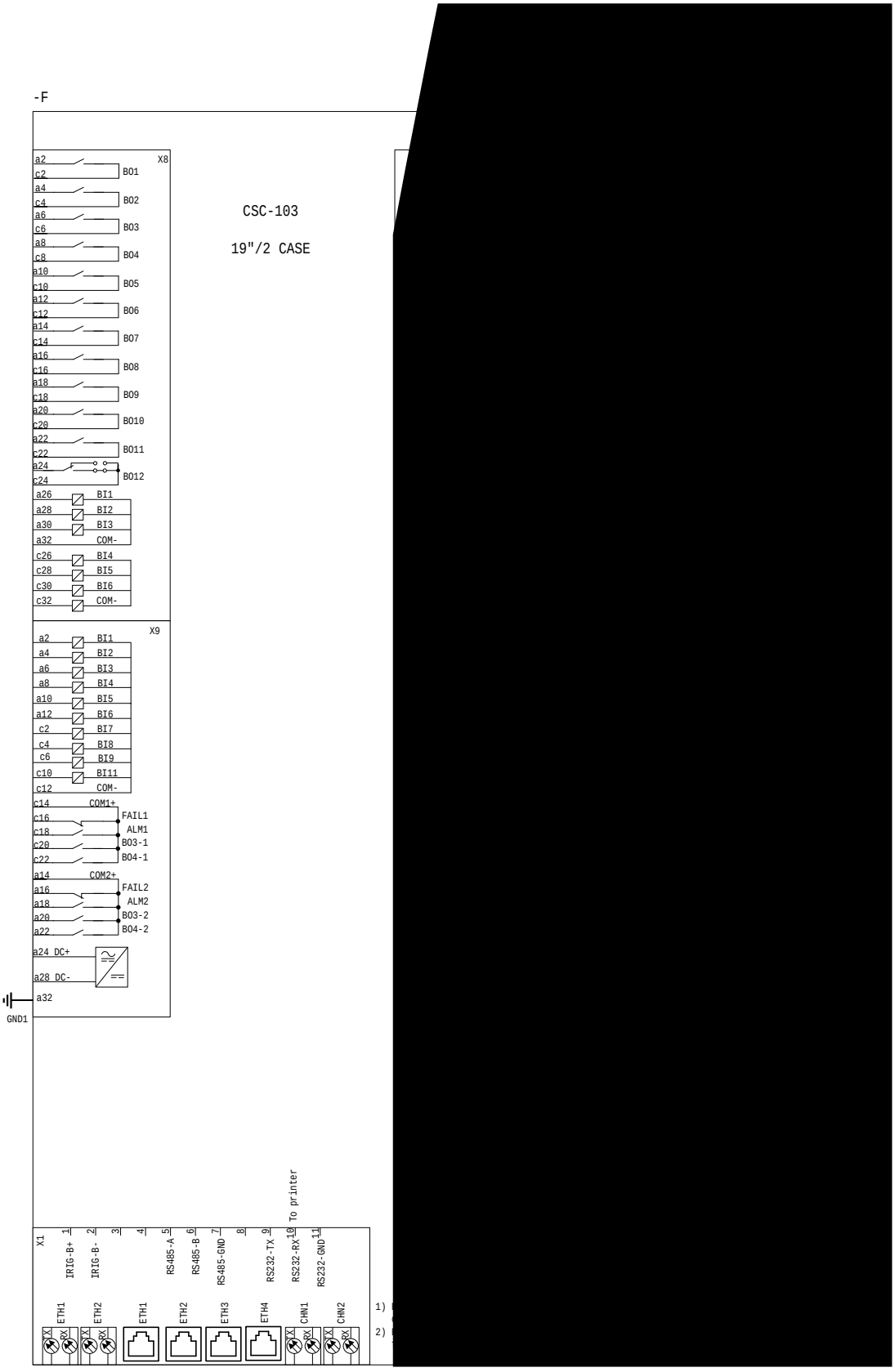
Item	Data
Shots of reclosing	1 to 4
Reclosing initiated inputs	Internal protection startups External binary input
Dead time for each shot of reclosing	s~600.00s, step of 0.01 s
Reclosing confirmation time	0.10 s~100.00s, step of 0.01 s
Reclosing charging time	0.05 s~100.00s, step of 0.01 s
Equal breaking time	0.10 s~100.00s, step of 0.01 s
Disable	

97FF, VT secondary circuit supervision

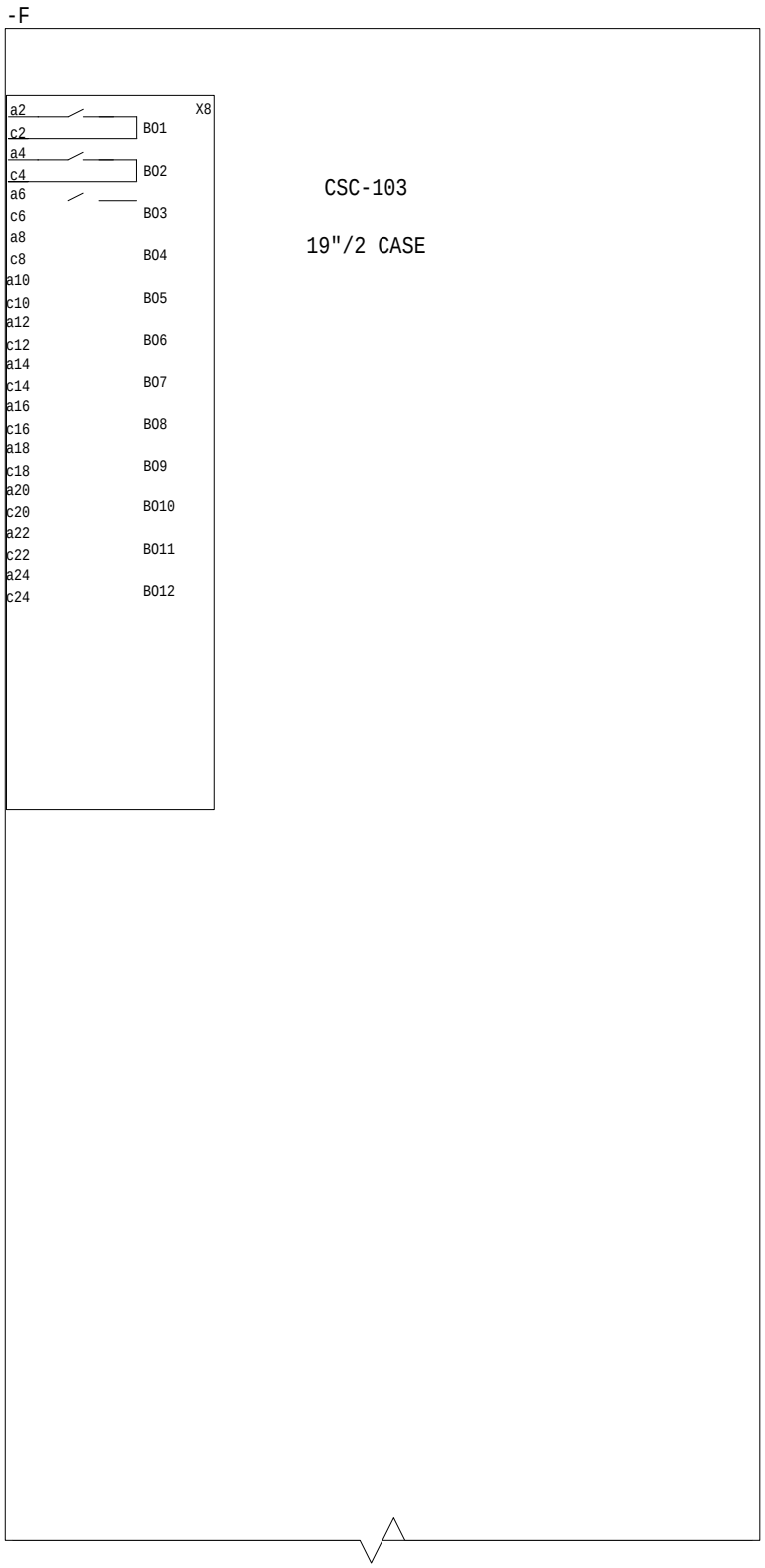
Item	Data

Connection

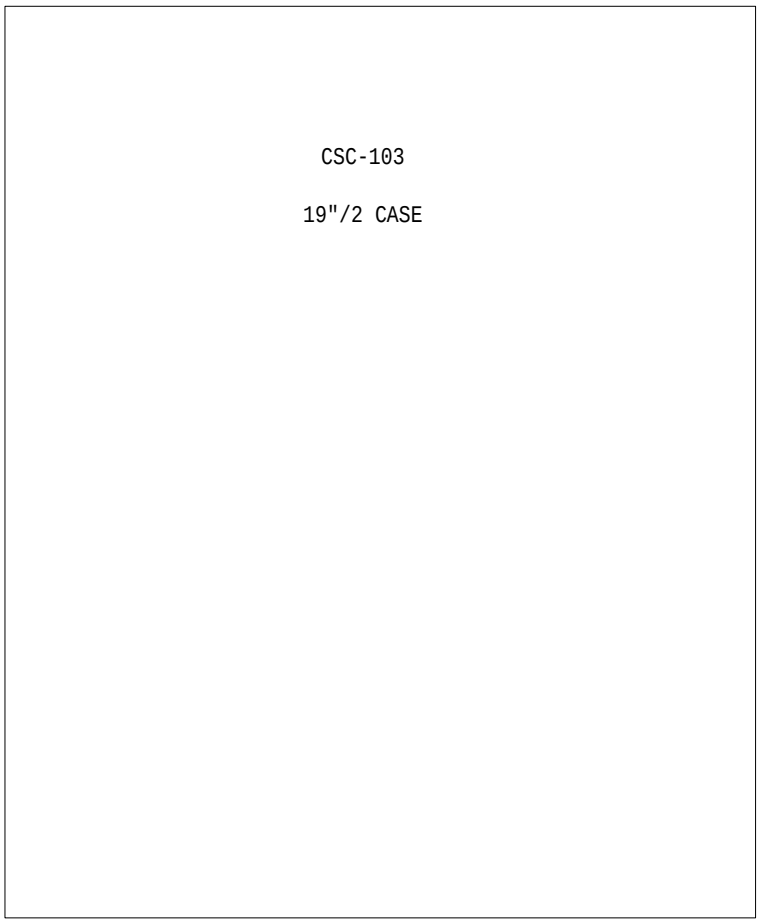
Typical configure 1



Typical configure 2



Connection



Typical configure 3

